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RF CMOS - Wikipedia

The simple diode varactor circuit is adopted for center-frequency tuning. These two improved circuits can reduce the power dissipation of the amplifier. An experimental chip fabricated by 0.5 μm double-poly-double-metal CMOS technology occupies a chip area of $500 \times 500 \mu\text{m}^2$ chip area. The measured results have verified the performance of the ...

The design of a 2-V 900-MHz CMOS bandpass amplifier ...

The project for this class will involve the design and simulation of a down-conversion chain for a radio frequency receiver in a 0.18 μm CMOS process. The project will be split into three subprojects: the first will be to design a low noise amplifier (LNA), the second will be to design a mixer, and the third will be to design a VCO and combine the three

ECE 6730: Radio Frequency Integrated Circuit Design

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The Design of CMOS Radio-Frequency Integrated Circuits

Recent Advances and Design Trends in CMOS Radio Frequency Integrated Circuits7 LO ports, wherein a strong interferer can phase-modulate the LO and self-mix down to the baseband frequencies. To gain an appreciation for the severity of the problem, consider an application of direct-conversion to the GSM standard.

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