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Architecture. In a k -way set associative
cache, the cache is divided into v sets,
each of which consists of k lines. The
lines of a set are placed in sequence one
after another. The lines in set s are
sequenced before the lines in set $(s+1)$.

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The main memory blocks are numbered 0 onwards.

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1. Memory unit that communicates directly with the CPU is called the A) Main memory RISC (Photo credit: Wikipedia) B) Secon...

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architecture Notes, Summaries ...**

A) Computer organization B) Computer
design C) Computer architecture D)
Computer implementation. 8. Devices
that are used primarily to transport data
between the processor and the user are

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known as A) Networking devices B)
Basic storage devices C) Data
presentation device D) Data transfer
device. 9. Which statement is valid
about computer ...

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COMPUTER ORGANIZATION AND
ARCHITECTURE. Computer System
Architecture MCQ 02 1. _____ is a
command given to a computer to
perform a specified operation on some

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given data. a. An instruction b.
Command c. Code d. None of these 2. An
instruction is guided by _____ to perform
work according: a. PC b. ALU c. Both a ...

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Course Description & Objectives:

Computer architecture is a key component of computer engineering and the practicing computer engineer should have a practical understanding of this topic. It is concerned with all aspects of the design and organization of the

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central processing unit and the integration of the CPU into the computer system itself

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